

SANYO

No.2483A

2 S C 4 1 6 2

NPN Triple Diffused Planar Type
Silicon Transistor

SWITCHING REGULATOR APPLICATIONS

Features

- . High breakdown voltage, high reliability
- . Fast switching speed ($t_f=0.1\mu s$ typ)
- . Wide ASO
- . Adoption of MBIT process
- . Micaless package facilitating mounting

Absolute Maximum Ratings at $T_a=25^\circ C$

			unit
Collector-to-Base Voltage	V_{CB0}	500	V
Collector-to-Emitter Voltage	V_{CEO}	400	V
Emitter-to-Base Voltage	V_{EBO}	7	V
Collector Current	I_C	10	A
Peak Collector Current	i_{cp}	$PW \leq 300\mu s, \text{duty cycle} \leq 10\%$	A
Collector Dissipation	P_C	$T_c=25^\circ C$	35 W
Junction Temperature	T_j	150	$^\circ C$
Storage Temperature	T_{stg}	-55 to +150	$^\circ C$

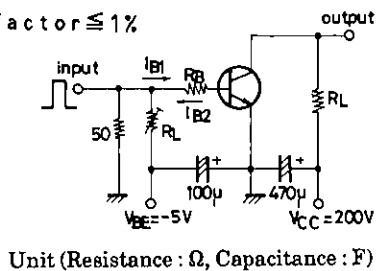
Electrical Characteristics at $T_a=25^\circ C$

		min	typ	max	unit
Collector Cutoff Current	I_{CBO}			10	μA
Emitter Cutoff Current	I_{EBO}			10	μA
DC Current Gain	h_{FE1}		15*	50*	
	h_{FE2}		10		
	h_{FE3}		10		
Gain-Bandwidth Product	f_T		20		MHz
Output Capacitance	c_{ob}		120		pF
C-E Saturation Voltage	$V_{CE(sat)}$			0.8	V
B-E Saturation Voltage	$V_{BE(sat)}$			1.5	V
C-B Breakdown Voltage	$V_{(BR)CBO}$	500			V
C-E Breakdown Voltage	$V_{(BR)CEO}$	400			V
E-B Breakdown Voltage	$V_{(BR)EBO}$	7			V

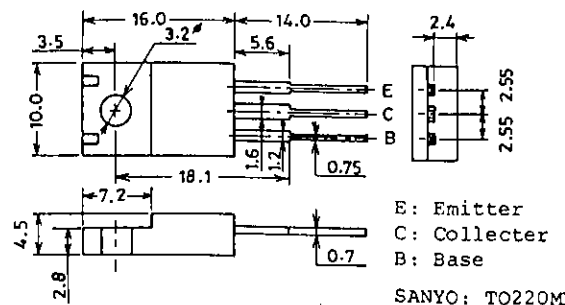
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Switching Time Test Circuit

$PW=20\mu s, \text{duty factor} \leq 1\%$



Package Dimensions 2041 (unit:mm)

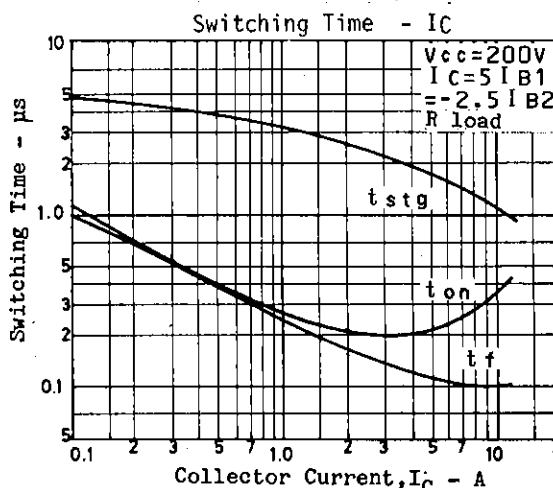
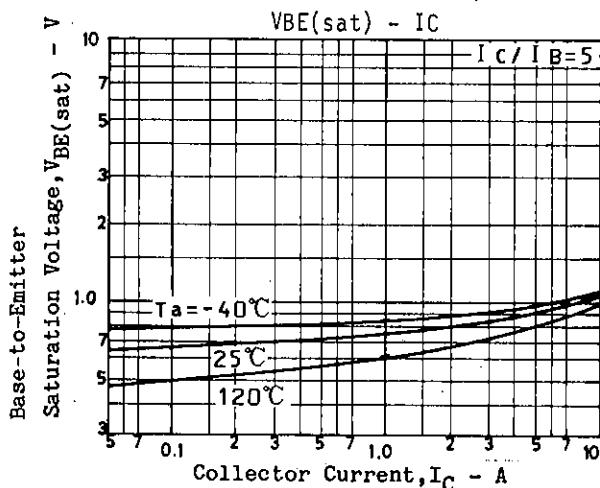
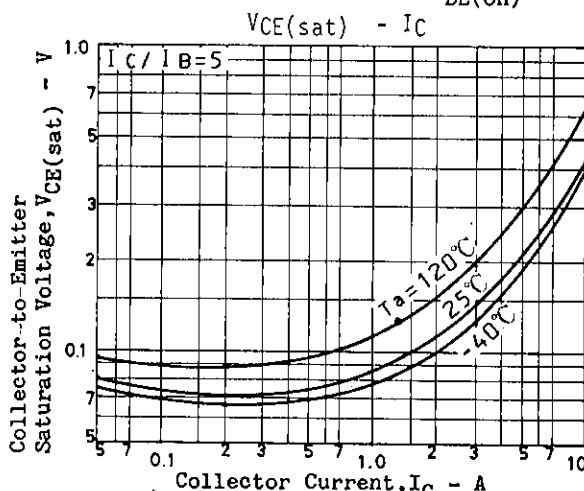
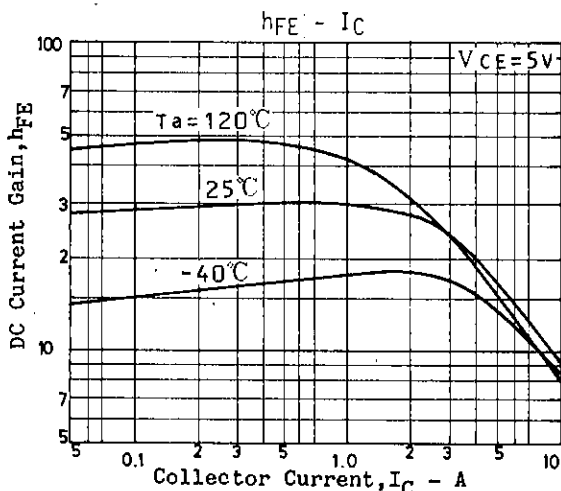
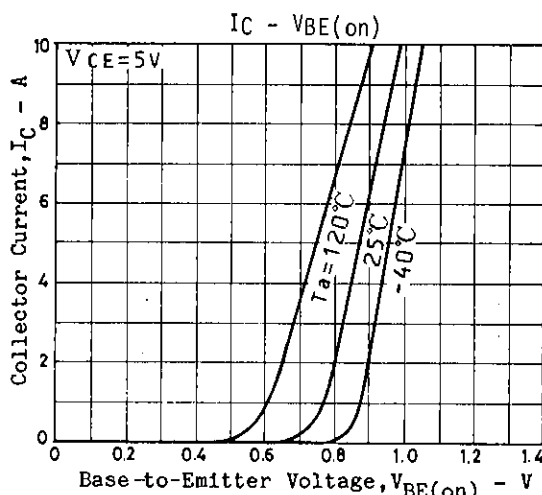
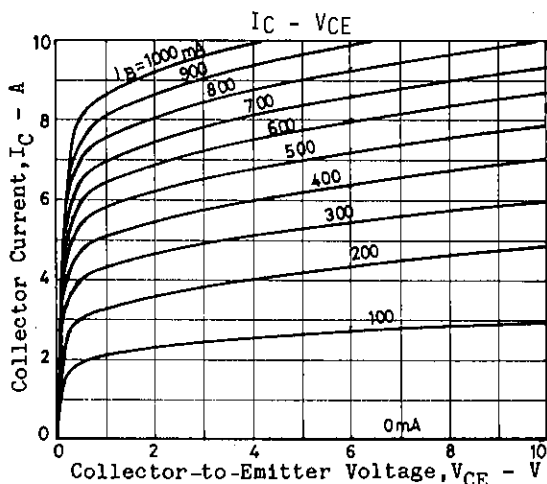


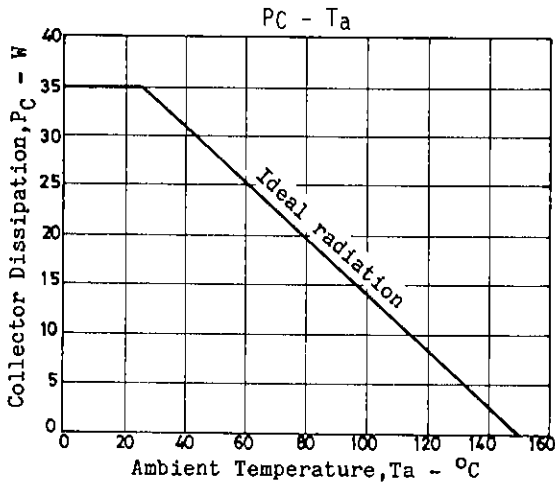
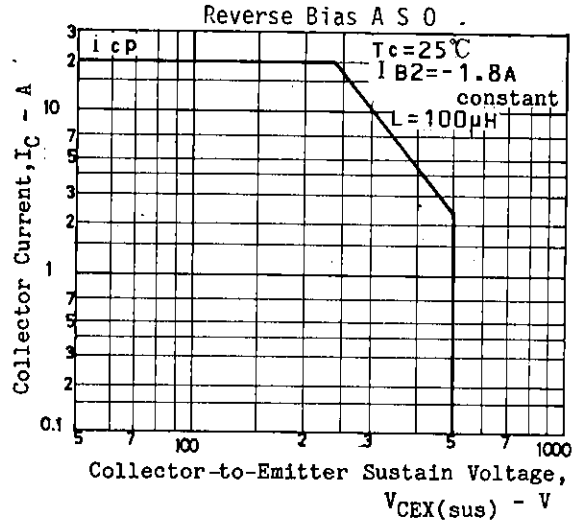
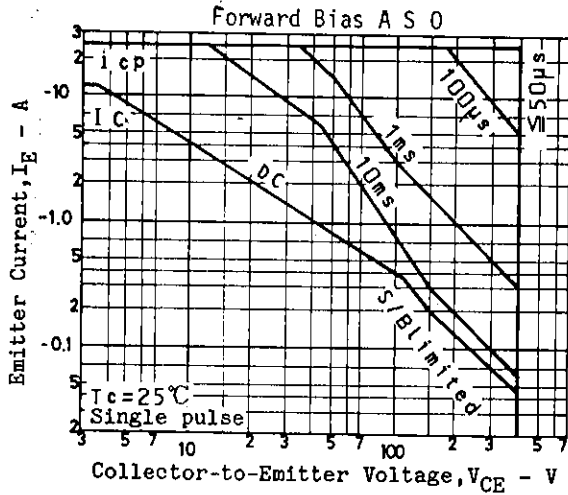
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			min	typ	max	unit
C-E Sustain Voltage	$V_{CEX(sus)}$	$I_C=4.5A, I_{B1}=0.45A,$ $I_{B2}=-1.8A, L=500\mu H, \text{clamped}$	400			V
Turn-on Time	t_{on}	$I_C=7A, I_{B1}=1.4A, I_{B2}=2.8A,$ $R_L=28.6\text{ohms}, V_{CC}=200V$			0.5	μs
Storage Time	t_{stg}	" "			2.5	μs
Fall Time	t_f	" "			0.3	μs

*: The h_{FE1} of the 2SC4162 is classified as follows. When specifying the h_{FE1} rank, specify two ranks or more in principle.

15	L	30	20	M	40	30	N	50
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